

DS-02-021



October 6, 2003

To: Commissioner for Patents  
P.O.Box 1450  
Alexandria, VA 22313-1450

Fr: George O. Saile, Reg. No. 19,572  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 10/615,123 07/08/03

Rainer Krenzke

SLEW RATE SENSING AND CONTROL OF A  
HIGH-VOLTAGE OUTPUT DRIVER FOR A  
VARIABLE VOLTAGE RANGE AND VARIABLE  
OUTPUT LOAD

Grp. Art Unit:

#### INFORMATION DISCLOSURE STATEMENT

Enclosed is Form PTO-1449, Information Disclosure Citation  
In An Application.


The following Patents and/or Publications are submitted to  
comply with the duty of disclosure under CFR 1.97-1.99 and  
37 CFR 1.56. Copies of each document is included herewith.

#### CERTIFICATE OF MAILING

I hereby certify that this correspondence is being  
deposited with the United States Postal Service as first class  
mail in an envelope addressed to: Commissioner for Patents,  
P.O. Box 1450, Alexandria, VA 22313-1450, on October 10, 2003.

Stephen B. Ackerman, Reg.# 37761

Signature/Date

 10/10/03

DS-02-021

DS-02-020 to Rainer Krenzke, Serial # 10/614,663, Filed 07/07/03, "Comparator with High-Voltage Inputs in an Extended CMOS Process for Higher Voltage Levels," discusses comparator circuits realized with integrated-circuit technologies.


U.S. Patent 6,181,193 to Coughlin, Jr., "Using Thick-Oxide CMOS Devices to Interface High Voltage Integrated Circuits," describes using thick-oxide CMOS devices to interface high voltage integrated circuits, especially a high voltage tolerant CMOS input/output interface circuit.

U.S. Patent 6,201,417 to Blum et al., "Shaping a Current Sense Signal by Using a Controlled Slew Rate," discloses the shaping of a current sense signal by using a controlled slew rate, a method and circuit for reducing the leading edge spike in a current sense signal.

U.S. Patent 6,476,654 to Tanaka, "Slew Rate Adjusting Circuit," discloses a slew rate adjusting circuit.

U.S. Patent 6,483,386 to Cress et al., "Low Voltage Differential Amplifier with High Voltage Protection," discloses a low voltage differential amplifier with high voltage protection.

Sincerely,

  
Stephen B. Ackerman,  
Reg. No. 37761

DS-02-021

10/615,123

Applicant

Rainer Krenzke

Filing Date

07/08/03

Group Art Unit

INFORMATION DISCLOSURE CITATION  
IN AN APPLICATION

(attach several sheets if necessary)

## U. S. PATENT DOCUMENTS

EXAMINER W. PATENT & TRADEMARK	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	6181193	1/30/01	Coughlin, Jr.	327	534	10/8/99
	6201417	3/13/01	Blum et al.	327	14	9/2/94
	6476654	11/5/02	Tanaka	327	170	7/12/01
	6483386	11/19/02	Cress et al.	330	298	9/29/00

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
					YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Portfolios Pages, Etc.)

DS-02-020 to Rainer Krenzke, Serial # 10/614,663, Filed 07/07/03, "Comparator with High-Voltage Inputs in an Extended CMOS Process for Higher Voltage Levels".

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.